

CCPIS2023

INTERNATIONAL CONFERENCE ON CIRCUITS, POWER, AND INTELLIGENT SYSTEMS

Organized by:

Silicon Institute of Technology, Bhubaneswar Odisha, India



1st-3rd September 2023

Important Dates

Paper Submission Deadline: Notification of Acceptance:

July 5, 2023 Early Bird Registration Closes: July 20, 2023 Late Registration Closes: **August 10, 2023** Camera-ready Submission: **August 10, 2023**

June 10, 2023 (New Deadline)

Paper Submission Link

https://ccpis.in/paper_submission.html





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Technical Sponsors



Technical papers are invited on the subjects pertaining to the scope of the conference that includes, but not limited to the following topics:

Track 1: Microelectronic Circuits & Systems Design

- Analog, Mixed Signal and RF Circuits & Systems Design
- Digital VLSI System Design, Testing and Verification.
- CMOS and Emerging Technologies
- Memory Circuits, Architectures, and Interfaces
- FPGA-based Designs
- Circuits, Signal Integrity, and IOs
- Nanoelectronics, Semiconductor Devices Modelling & Circuit co-design
- Algorithms and CAD for Electron Devices and Circuits
- MEMS
- Sensors, Sensor based Systems & applications

Track 3: Multimedia and Signal Processing

- Signal Processing Systems and Applications
- Speech, Audio and Acoustic Signal Processing
- Statistical, Adaptive and Array Processing
- Image, Video & Multimedia Signal Processing
- Radar and Sonar Signal Processing
- Bioinformatics and biomedical Signal Processing
- ML and Al based Signal Processing

Track 5: Power, Energy, and Control

- Power Electronics and Drives
- Electrical Machines
- Renewable Energy and Storage Systems
- Power System Modelling, Operation, Optimization
- IoT in Power System Application
- Microgrid and Smart Grid
- High Voltage Engineering
- Robust, Adaptive and Non-linear Control Systems
- Electric Vehicles and the Impact on the Grid
- Application of Data Analytics in Energy Management
- Hydrogen as a Future Energy Resource

Paper and Presentation awards

Best paper awards based on number of technical tracks.

Address for Correspondence

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Registration Fees

SI. No.	Category	Indian Delegates (in INR)		Foreign Delegates (in USD)	
		Non-IEEE	IEEE	Non-IEEE	IEEE
1	Industry/R&D/Academic/Faculty	₹ 6500	₹ 5500	\$ 250	\$ 200
2	Student/Research Scholar	₹ 5500	₹ 4500	\$ 150	\$ 100
3	Attendee	₹ 1000		\$ 25	

Track 2: Network and Distribution Systems

- Multicore and processor architectures
- On-chip and multi-chip interconnection networks
- Biologically inspired massively parallel architectures
- Power-aware design and computing
- Networked sensor devices and systems
- Embedded systems design
- Real-time systems
- Service-Oriented Architecture (SOA) for embedded
- Network, cyber security and Cyber-physical systems
- Blockchain, Cryptography and security solutions

Track 4: Communication Network and IoT

- Wireless Communication, Networks and Security
- Vehicular, Ad-hoc and Cooperative Communication
- Optical Communication and Networks
- Cognitive Radio Networks
- 5G/6G, IoT Applications and Security Networks
- Low/High-Power Microwave Devices and Circuits
- Emerging trends in Antenna design
- Microwave Photonics and Optics

Track 6: Intelligent Systems & Applications

- Data Analytics
- Machine Learning and Deep Learning
- Big Data Analysis and Algorithms
- Pattern recognition and Data Mining
- Natural Language Processing
- Social Network Analysis
- Image Processing, Computer Vision and Robotics
- Industry 4.0, HCI, and Automation

Publication and Submission

- All papers must be original, and not simultaneously submitted to another publication. Authors are requested to submit their papers in not more than 6 pages, prepared in the two column IEEE format (Check here for IEEE Conference Templates https://www.ieee.org/conferences/publishing/templates.html).
- Similarity index below 20% is acceptable for review.
- · All accepted and presented papers will be submitted for inclusion into IEEE Xplore digital library subject to meeting IEEE Xplore's standards for scope and/or quality requirements.
- Paper Submission Link https://ccpis.in/paper_submission.html

Venue

Silicon Institute of Technology, Bhubaneswar Odisha, India



